

New generation of cage‐type current shunts developed using model analysis

Věra Nováková Zachovalová, Martin Šíra, Pavel Bednář, Stanislav Mašláň

Czech Metrology Institute, Okruzni 31 638 00 Brno, Czech Republic

ABSTRACT

This paper describes a new generation of cage-type ac-dc current shunts from 30 mA up to 10 A, developed using a lumped circuit element model. Comparison of the calculated and measured values shows agreement better than 6 ppm in the ac‐dc difference at frequencies up to 100 kHz.

Section: RESEARCH PAPER

Keywords: Current shunt; Current measurement; Phase measurement; Electric variables measurement

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Corresponding author: Věra Nováková Zachovalová, e‐mail: vnovakovazachovalova@cmi.cz

1. INTRODUCTION

At national metrology institutes (NMI) shunts of special designs are used for high accuracy current measurements converting a current from the working level to a voltage convenient for the input of a meter. These shunts are mainly used in power and power quality measurement systems and in ac-dc current transfer difference measurement systems. The acdc current transfer difference serves for derivation of a root mean square (RMS) value of an AC current from a dc current of the same nominal level It can be defined as the relative difference between the RMS value of the ac current and the dc current of the same nominal level and can be measured by devices called thermal converters.

In recent years the shunts of three different designs for high accuracy current measurements were developed at several NMIs:

- foil shunts [1] are built using resistive manganin or zeranin foils. They are distinguished by a lower ac-dc difference and phase angle error compared with cage type current shunts but on the other hand their temperature and power coefficient are larger.
- cage shunts [2]-[6] are built using a symmetrical construction of printed circuit board (PCB) with insoldered discrete resistors. They are characterized by

low temperature and power coefficients, but their ac-dc difference and phase angle error are larger compared with foil shunts.

 coaxial shunts [7] are made using disk structures, either with surface mount (SMD) resistors or resistive layers. Their design is really compact but their frequency characteristic is the worst of all three shunt designs.

At the Czech Metrology Institute (CMI) the cage design was used to build a first (original) series of current shunts in 2008 [6]. In subsequent research a calculable model of the CMI cage shunt was developed using lumped circuit elements. The transimpedance, ac-dc difference and also the phase angle error of a shunt can be derived from the model [9].

The goal of this research was to build a new generation of cage shunts from 30 mA up to 10 A based on a model analysis for the purpose of optimizing their construction to achieve the lowest possible ac-dc difference and phase angle error.

2. CMI CAGE‐TYPE CURRENT SHUNTS CONSTRUCTION AND MODELING

The original CMI's shunts were developed to be suitable for use with planar multijunction thermal converters (PMJTC) to establish an AC-DC current transfer difference measurement system at CMI in 2008 [6].

The voltage drop across every shunt in parallel with a 90 Ω PMJTC is 1 V at nominal current. The number, value and type of resistors are different in each shunt (see Table 1). The temperature coefficient of the shunts was reduced by using a suitable combination of resistors S102 with different temperature coefficients (one third of S102C and two thirds of S102K), except for the 30 mA shunt with Z201 resistors, which temperature coefficient is below 1 ppm/K [6], [8].

The physical design of the original CMI shunts is shown in Figure 1 (detailed in Figure 2).

The shunts are constructed using single-sided and doublesided fibreglass-epoxy PCB material (FR4 with permittivity 5.45 and loss factor 0.0205) of 2 mm thickness with mounted Vishay foil Z201 or S102 resistors and can be split up in the following construction parts [6], [9]:

- input connector;
- input part (two disks made of single-sided PCB);
- mput part (two disks made of single-sided PCB);
crossbars made of double-sided PCB;

Figure 1. The original CMI shunts.

Figure 2. The design of the original 100 mA shunt.

Table 1. General parameters of the original shunts.

| Nominal current | Resistance (Ω) | Number and value of resistors | Type of resistors | PCB type |
|---------------------------|-------------------|-------------------------------------|------------------------|----------|
| 30 mA | 50 | $3x$ 150 Ω | Z201 | FR4 |
| 100 mA | 10 | $10x100\Omega$ | 3x S102C 7x S102K | FR4 |
| 300 mA | 3.3 | $30x100\Omega$ | 10x S102C 20x S102K | FR4 |
| 1 A | $\mathbf{1}$ | $100x100\Omega$ | 33x S102C 67x S102K | FR4 |
| 10 A | 0.1 | 100x100 | 33x S102C 67x S102K | FR4 |

- output part (circle and disk made of single-sided PCB and an aluminium wire connected between circle and output connector);
- output connector.

The input connector brings the current to the input disks, which spread the current through the crossbars. Each crossbar carries a fraction of the input current to resistors connected in parallel. The output part senses the voltage drop across the resistors.

To each construction part a two-port element was assigned and their cascade concatenation led to the development of the shunt model (see Figure 3). Each two-port was determined based on the physical design of the related construction part, for instance the crossbars are made from double-sided PCB with a leakage capacitance and resistance C_b and R_{pb} between the copper layers, and the copper layer also has some inductance L_b and resistance R_b) [9].

The product of the cascade matrixes assigned to the twoports defines the cascade matrix of the model [9]:

$$
\hat{A}_{\rm S} = \hat{A}_{\rm CI} \hat{A}_{\rm I} \hat{A}_{\rm B} \hat{A}_{\rm R} \hat{A}_{\rm O} \hat{A}_{\rm CO}
$$
\n(1)

where $\hat{A}_{\text{CI}}, \hat{A}_{\text{I}}, \hat{A}_{\text{B}}, \hat{A}_{\text{R}}, \hat{A}_{\text{O}}$ and \hat{A}_{CO} are cascade matrixes of individual two-ports according to Figure 3.

During measurements the output of the shunt is loaded by the device that measures the voltage drop across the shunt. Therefore it is necessary to include this load into the model. Then, the cascade matrix of the loaded shunt model is [9]:

$$
\hat{A} = \hat{A}_{\rm S}\hat{A}_{\rm L} = \begin{bmatrix} \hat{a}_{11} & \hat{a}_{12} \\ \hat{a}_{21} & \hat{a}_{22} \end{bmatrix} \tag{2}
$$

where \hat{A}_{L} is cascade matrix assigned to the load and \hat{a}_{11} , \hat{a}_{12} , \hat{a}_{21} and \hat{a}_{22} are elements of matrix \hat{A} . Element \hat{a}_{21} can be

Figure 3. The lumped element model of the shunts.

used for calculation of the transimpedance of the shunt [9]:

$$
\hat{Z}_{\mathrm{T}} = \frac{1}{\hat{a}_{21}}.\tag{3}
$$

From the transimpedance the phase-angle error and the acdc difference can be derived easily [9].

This model is based on calculations of all component values from the geometry and material properties. Therefore it was necessary to be familiar with the dielectric properties of the PCB (permittivity, loss factor) and the capacitance and inductance of the resistor, which had to be measured [9].

Evaluation of associated uncertainties of the model was done by means of the Monte Carlo method [9].

A detailed description of the model calculation is given in [9].

 The model was validated by comparison of the calculated and measured values of the original shunts which showed agreement better than 6 ppm in the ac-dc difference and 110 µrad in the phase angle error at frequencies up to 100 kHz [9].

3. CONSTRUCTION IMPROVEMENTS

The shunt model can be used to determine the sensitivity of the output quantities to the modification of the input quantities, resulting in improvements to the construction of the shunts [9].

3.1. Theoretical analysis

The sensitivity of the calculated values of the ac-dc difference and phase angle error to modification of the input quantities of the model was investigated using parametric simulation.

Calculations indicated that some of the input quantities have a significant influence and some have an insignificant influence on the calculated values of the ac-dc difference and phase angle error. The following input quantities of the model were found to have significant influence on the calculated values [9]:

- capacitance and inductance of the resistors;
- relative permittivity and loss factor of the PCB;
- thickness of the PCB;
- number of crossbars and geometric dimensions.

It was found that the influence of all these input quantities increases with frequency (see the example of the ac-dc difference and phase angle error dependence on frequency with modification of the PCB permittivity in Figure 4 and Figure 5).

By converting the data to a single frequency point it was ascertained that the dependence of the ac-dc difference and phase angle error on the modification of the relevant input quantities is linear except for the dependence on the modification of PCB thickness (see the example of the ac-dc difference and phase angle error dependence on modification

Figure 4. Ac‐dc difference dependence on frequency with modification of PCB permittivity calculated for a 100 mA shunt.

Figure 5. Phase angle error dependence on frequency with modification of PCB permittivity calculated for a 100 mA shunt.

of PCB permittivity and PCB thickness in Figure 6 to Figure 9, calculated at 100 kHz).

In the next step the calculation of sensitivity coefficients was performed to extract information about the vastness of influence of a single input quantity. The sensitivity coefficients were calculated as:

Figure 6. Ac-dc difference dependence on modification of the PCB permittivity calculated at 100 kHz.

Figure 7. Phase angle error dependence on modification of the PCB permittivity calculated at 100 kHz.

Figure 8. Ac-dc difference dependence on modification of the PCB thickness calculated at 100 kHz.

Figure 9. Phase angle error dependence on modification of the PCB thickness calculated at 100 kHz.

where Δy represents a change of the output quantity (ac-dc difference or phase angle error) corresponding to a change Δx of the selected input quantity.

Ac-dc difference and phase angle error sensitivity coefficients calculated at 100 kHz are shown in Table 2 and Table 3. The values of the sensitivity coefficients are different for the low-current and high-current shunts. This indicates that the optimization of the shunts construction depends on different input quantities for the low-current and high-current shunts construction.

 The ac-dc difference of the low-current shunts is affected by the dielectric properties and thickness of the PCB and slightly also by geometrical dimensions. The ac-dc difference of high-current shunts depends more on the disk size and less on the dielectric properties and thickness of the PCB. The

Table 2. Ac‐dc difference sensitivity coefficients calculated at 100 kHz.

| Input quantity | 30 mA shunt | 100 mA shunt | 1 A shunt | 10 A shunt | Unit of sensitivity coefficient |
|--------------------------|----------------|-----------------|--------------|---------------|---------------------------------------|
| Capacitance of resistors | 0.4 | 0.1 | 0.0 | 0.0 | ppm/pF |
| Inductance of resistors | 0.0 | 0.0 | 0.0 | -0.1 | ppm/nH |
| PCB thickness | -44.3 | -11.3 | -2.9 | 3.2 | ppm/mm |
| PCB permittivity | 30.2 | 5.1 | 1.3 | -3.2 | ppm |
| PCB loss factor | 57.8 | 12.9 | 4.0 | 1.1 | ppm/ 10^{-2} |
| With of crossbars | 5.5 | 1.8 | 0.5 | 0.1 | ppm/mm |
| Length of crossbars | 2.8 | 0.4 | 0.1 | 0.0 | ppm/mm |
| Size of disks | 21.4 | 3.1 | -0.3 | -5.3 | ppm/cm |

Table 3. Phase angle error sensitivity coefficients calculated at 100 kHz.

capacitance and inductance of the resistors do not influence the ac-dc difference of all shunts.

The phase angle error of the low-current shunts is influenced by the relative permittivity and thickness of the PCB, by geometric dimensions and slightly also by the capacitance and inductance of the resistors. The phase angle error of highcurrent shunts is affected mainly by the inductance of the resistors, but also by the thickness of the PCB and slightly by the relative permittivity. The influence of the loss factor on the phase angle error is negligible for all shunts.

3.2. Construction of the new generation cage‐type current shunts

Figure 10 shows the new generation of cage-type shunts from 30 mA up to 10 A developed at CMI. The voltage drop across every shunt is 0.6 V at nominal current being convenient for use either with PMJTC in the measurement set up of the acdc current transfer difference established in 2008 [6] or with analogue to digital converters (ADC) in view of the development of a new sampling wattmeter measurement system.

The improvements to the shunts construction resulted from the theoretical analysis of the lumped element model described above. General information about resistors and PCB material used for their construction are in Table 4.

The low current shunts (up to 1 A) were constructed using the high frequency PCB material RO4350B with better dielectric properties (permittivity = 3.6, loss factor = 0.0031) than the previously used FR4 material. The thickness of the PCB was reduced from 2 mm down to 1.524 mm. Geometrical

Figure 10. The new generation of cage-type shunts.

Table 4. General parameters of the new shunts.

| Nominal current | Resistance (Ω) | Number and value of resistors | Type of resistors | PCB type |
|--------------------|-------------------|-------------------------------------|------------------------|----------|
| 30 _{mA} | 20 | $3x 60 \Omega$ | Z201 | RO4350B |
| 100 mA | 6 | $10x$ 60 Ω | 3x S102C 7x S102K | RO4350B |
| 300 mA | \mathfrak{p} | $30x 60 \Omega$ | 10x S102C 20x S102K | RO4350B |
| 1 A | 0.6 | 50x 30 Ω | 17x S102C 33x S102K | RO4350B |
| 10 A | 0.06 | $100x$ 6 Ω | 33x S102C 67x S102K | FR4 |

dimensions should be kept as small as possible to obtain low frequency dependence of the shunt transimpedance. The dimensions of the 30 mA shunt were slightly reduced to be the same as for the 100 mA shunt. Also the number of the 30 mA shunt crossbars was minimized to only three and the 30 mA shunt construction was reinforced using distance struts. The dimensions of the 100 mA, 300 mA and 1 A shunts were not modified. The length of the crossbars was reduced in all shunts.

The 10 A shunt was constructed using low frequency PCB (FR4) because of the inconsiderable influence of its dielectric properties to the calculated values. Only the thickness of the FR4 was reduced to 1.5 mm. Dimensions of the 10 A shunt were reduced to be the same as the original 1 A shunt.

The length of the crossbars was also reduced to be as short as possible. Resistors mounted in the shunts are the same type as in previous construction because no other resistors with lower inductance were found.

Including all of these modifications into the model showed a reduction in the ac-dc difference below 3.5 ppm at frequencies up to 100 kHz (see Table 5).

4. COMPARISON OF CALCULATED AND MEASURED VALUES OF THE NEW SHUNTS

The ac-dc difference of the shunts was measured using an automated measurement system developed for the ac-dc current transfer difference set up (see Figure 11), which was established in 2008 [6] and improved in 2010 [8].

A reference and a calibrated standard are connected in series. Ac and dc currents are applied through a transconductance amplifier Clarke&Hess 8100, which is alternately connected to a dc and ac voltage source (multifunction calibrators Fluke or Datron) using an automated switch OFMET. The standards are comprised of a shunt loaded by a thermal converter [8].

The uncertainty budget of such a complicated measurement set up covers contributions of the reference standard and its level dependence, the reproducibility of the measured ac-dc difference (standard deviation), influences of the series connection of both standards and measurement set up, the

Figure 11. The measurement system of the ac-dc current transfer difference.

frequency dependence, and the influence of temperature. A detailed description of the uncertainty calculation can be found in [8].

During the measurements of the new generation of the shunts the output of the shunts was loaded by a 90 $\Omega/10$ mA PMJTC, which was included in the model.

Table 5 shows the agreement between the measured and calculated values of the ac-dc differences of the new shunts to be better than 6 ppm at frequencies up to 100 kHz.

Table 6 shows the comparison of the measured ac-dc differences of the original and new shunts. For all shunts a significant reduction of the ac-dc difference was accomplished. The most significant reduction of the ac-dc difference was achieved for the 30 mA shunt, which construction was modified most of all shunts (see Section 3.2). Following the sensitivity coefficients calculation (see Table 2) the 30 mA shunt construction is also most sensitive to the modification of the input quantities in comparison with the other shunts. Because of the significant modification of the 30 mA shunt construction and its high sensitivity to these modification its ac-

Table 5. Calculated and measured values of the ac-dc difference for the new shunts with associated uncertainties for k=1.

Table 6. Comparison of the measured ac-dc difference values (ppm) of the original and new shunts generation.

dc difference was decreased by more than 100 ppm.

5. CONCLUSIONS

A new generation of the cage-type current shunts from 30 mA up to 10 A was developed.

Construction improvements were done based on the results obtained from the analysis of the lumped element model of the original shunts.

Comparison of the calculated and measured values of the new shunts showed an agreement better than 6 ppm in the acdc difference at frequencies up to 100 kHz.

Future work will address the comparison of the calculated and measured phase angle errors of the new shunts and the development of high current shunts up to 100 A.

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