

Stator winding fault diagnosis in permanent magnet synchronous generators based on short‐circuited turns identification using extended Kalman filter

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ABSTRACT

This paper deals with an Extended Kalman Filter based fault detection for inter‐turn short‐circuits in Permanent Magnet Synchronous Generators. Inter-turn short-circuits are among the most critical faults in the PMSG. Indeed, due to permanent magnets, the shortcircuit current is maintained as long as the machine is rotating. Thus, a specific PMSG faulty model in the d‐q frame is developed to estimate the number of short‐circuited turns which are used to build a fault indicator. Simulation results demonstrate the sensitivity and the robustness of the proposed fault indicator against various operation points on an electrical network even for a few number of short‐circuited turns.

Section: RESEARCH PAPER

Keywords: Extended Kalman Filter; fault diagnosis; on‐line parameters estimation; Permanent Magnet Synchronous Generator; inter‐turn short‐circuit

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1. INTRODUCTION

Permanent Magnet Synchronous Generators (PMSG) are increasingly used in various industrial fields such as aerospace, railway and automotive sectors or renewable energy [1] to take advantage of their high efficiency, small size and easy control for variable-speed applications compared to a wounded rotor design. With regard to safety considerations, the main drawback of PMSG is the critical effect of inter-turn short-circuits in stator windings. Indeed, stator faults in PMSG still remain while the machine is rotating because of the permanent rotor flux caused by magnets. More specifically, inter-turn short-circuits in stator windings are tricky to detect and are the root of highest fault currents, especially for a few number of faulty turns [2].

Thus, an early on-line detection is required to perform

the appropriate safety request and to avoid greater damage. As well as being quick, the detection method should be robust to electrical network variations to avoid false alarms. Moreover, the constraints related to on-line monitoring considerations imply a detection algorithm matching the performances of on-board computers.

In the literature, three main approaches are commonly used to detect inter-turn short-circuits in stator windings. A first approach, based on signal analysis, often uses spectral tools to underline specific frequency components related to the fault. For example, stator current analysis in Park's [2], Concordia [3] or three-phase's [4] frame and axial flux leakage measurements [5] can be used as an inter-turn shortcircuit fault indicator. Even if these methods are compatible with on-line requirements and enable relatively quick fault detection, they are not suitable for variable-speed

applications and are not robust with regard to electrical network variations in generator operation.

The second approach is related to knowledge-based methods which imply an *a priori* knowledge of fault signature to allow an *a posteriori* fault classification during the monitored system operation. Artificial intelligence tools as fuzzy logic system [6], artificial neural networks [7] and pattern recognition methods [8] can be used but time response and computation considerations are not matching with on-line detection requirements.

The third approach is based on state or parameters estimation and implies the use of mathematical models of the studied system [9]. These methods are a good compromise between robustness with regard to electrical network variations, on-board computation and fast fault detection. To meet these requirements, the chosen method presented in this paper for inter-turn short-circuit detection is based on the identification of the short-circuited turns ratio in a faulty PMSG model expressed in Park frame using an Extended Kalman Filter (EKF) algorithm. Traditionally used for state or parameter estimation, the EKF algorithm is, in this case, specifically adapted for detection purposes. A basic modelling approach allows defining a generalized algorithm suitable for a large power scale of synchronous generators. No additional measurements are required because the proposed detection scheme is fed with measurements already available for control purposes.

This paper is organized as follow: in section 2, the faulty PMSG Park model used for fault diagnosis is described. Section 3 presents simulation results of the EKF algorithms in both healthy and faulty conditions. The proposed fault indicator built is also described. Finally, a robustness and a sensitivity analysis are presented in section 4 to assess the proposed indicator effectiveness.

2. FAULTY PMSG MODEL FOR INTER‐TURN SHORT‐CIRCUIT DETECTION

The faulty PMSG model used for inter-turn short-circuit detection is based on an earlier study [10] with less modelling assumptions (voltage drops due to short-circuit reduction is taken into account) to make the PMSG model more sensitive to windings faults. The model enables the fault localization with the use of angle $\theta_{s/c}$ (equal to 0, $2\pi/3$ or $4\pi/3$ for a short-circuit respectively on phase A, phase B or phase C) and the number of short-circuit turns *ns/c* (ratio

between short-circuited turns and all turns on a stator winding). Figure 1 shows this model used for the fault diagnosis with inter-turn short-circuit located on phase C $(\theta_{s/c} = 4\pi/3)$.

According to this faulty model, the stator voltages [*VS*] and the short-circuit loop equations are respectively given in (1) and (2) :

$$
[V_{S}] = -R_{S} [I_{S}] - n_{s/c} R_{S} T_{s/c} I_{s/c} - [\dot{\phi}_{S}]
$$
\n(1)

$$
0 = n_{s/c} R_{S} T_{s/c}^{T} [I_{S}] + n_{s/c} R_{S} I_{s/c} + \dot{\Phi}_{s/c}
$$
 (2)

where the stator flux [Φ s] and the short-circuit flux θ s/c are respectively expressed in (3) and (4):

$$
[\dot{\Phi}_{S}] = [L] \cdot [I_{S}] + \sqrt{\frac{3}{2} n_{s/c} L_{ps}} T_{32} \cdot \left[\frac{\cos \theta_{s/c}}{\sin \theta_{s/c}} \right] \cdot I_{s/c} + n_{s/c} L_{ls} T_{s/c} I_{s/c} \cdot [E]
$$
\n(3)

$$
\Phi_{s/c} = \sqrt{\frac{3}{2} . n_{s/c} . L_{ps} .} \left(T_{32} . \left[\frac{\cos \theta_{s/c}}{\sin \theta_{s/c}} \right] \right)^{T} . [i_S] + \n+ n_{s/c} . L_{ls} . T_{s/c}^{T} . [i_S] + n_{s/c}^{2} . (L_{ps} + L_{ls}) . I_{s/c} - n_{s/c} . T_{s/c}^{T} . [E]
$$
\n(4)

with

$$
[T_{32}] = \frac{1}{\sqrt{3}} \begin{bmatrix} \sqrt{2} & 0 \\ -1/\sqrt{2} & \sqrt{3}/2 \\ -1/\sqrt{2} & -\sqrt{3}/2 \end{bmatrix}
$$
: Concordia transformation

matrix

$$
T_{s/c} = \frac{1}{3} \cdot \left[\frac{1+2 \cdot \cos(\theta_{s/c})}{1+2 \cdot \cos(\theta_{s/c} - \frac{2\pi}{3})} \right] : \text{Short-circuit matrix}
$$

After Park transformation applied to (1) and (2) and some calculations, the faulty model can be expressed with these two equations (5) and (6) :

$$
[VS]dq = -RS.[IS']dq \omega.LS.[\begin{bmatrix} 0 & -1 \ 1 & 0 \end{bmatrix}.[IS]dq - LS.[IS]dq ++ [E]dq
$$
 (5)

$$
[I_{S}]_{dq} = [I'_{S}]_{dq} - [\tilde{I}_{s/c}]_{dq} = [I'_{S}]_{dq} - \frac{1}{[Z_{s/c}]_{dq}} \cdot [V_{S}]_{dq}
$$
(6)

where:

Stator synchronous inductance: $L_s = 3/2 \cdot L_{ps} + L_k$ Figure 1. Faulty PMSG model used for inter-turn short-circuit diagnosis. $\text{Park transformation matrix: } P(\theta) = \begin{bmatrix} \cos \theta & -\sin \theta \\ \sin \theta & \cos \theta \end{bmatrix}$ Short-circuit fault impedance:

$$
\frac{1}{\left[z_{\overline{c}}\right]_{dq}} = \frac{2.n_{\overline{c}}}{\left(3-2.n_{\overline{c}}\right).R_{S}}.P(\theta)^{T}.Q\left(\theta_{\frac{S}{c}}\right).P(\theta)
$$

Fault localization matrix:

$$
Q\left(\theta_{\frac{s}{c}}\right) = \begin{bmatrix} \cos^2\left(\theta_{\frac{s}{c}}\right) & \cos\left(\theta_{\frac{s}{c}}\right) \sin\left(\theta_{\frac{s}{c}}\right) \\ \cos\left(\theta_{\frac{s}{c}}\right) \sin\left(\theta_{\frac{s}{c}}\right) & \sin^2\left(\theta_{\frac{s}{c}}\right) \end{bmatrix}
$$

Short-circuit current calculation in Park frame:

$$
\left[\tilde{I}_{\overline{c}}\right]_{dq} = P(\theta)^{T} \cdot \sqrt{\frac{2}{3}} \cdot n_{\overline{c}} \cdot \left[\frac{\cos\left(\theta_{\frac{s}{c}}\right)}{\sin\left(\theta_{\frac{s}{c}}\right)}\right] \cdot I_{\overline{c}}\right]
$$

It can be noticed that (5) is the same equation as a classic healthy PMSG model taking into account *[IS']* as the stator current whereas the inter-turn short-circuit loop is represented in (6) with the equivalent fault impedance *[Zs/c]* at the output of the machine which deflects a part of the stator current.

Expending this model to each phase, three short-circuit impedances ($Z_{s/cA}$, $Z_{s/cB}$, $Z_{s/cC}$ for $\theta_{s/c}=0$, $2\pi/3$, $4\pi/3$ respectively) are added to the PMSG model as shown in Figure 2.

This model can be written as a state space representation with *[Is']* as the state vector. The inter-turn fault is expressed in the feedforward matrix *D*, depending on the number of short-circuited turns *ns/cA, ns/cB, ns/cC*.

To estimate these parameters in this non-linear model, the EKF algorithm is used. In the EKF algorithm, special attention should be paid to covariance matrices tuning, particularly for the measurement noise covariance matrix *R* and the state noise covariance matrix *Q* to obtain, at the same time, an accurate estimation and an appropriate dynamic response of the extended parameters [11]. In order to satisfy the fast dynamic response requirement for the fault indicator and according to noise measurements, the covariance matrices used for the estimation of each *ns/c* are expressed in (7):

$$
P_0 = 10^{-1} \cdot I_{5 \times 5}; R = 10^{-2} \cdot I_{2 \times 2}; Q = 10^{-5} \cdot \begin{bmatrix} I_{2 \times 2} & 0 \\ 0 & 10^{-4} \cdot I_{3 \times 3} \end{bmatrix} \tag{7}
$$

Simulations results are presented in the following section.

PMSG Characteristics			Stator windings
Rated Power	3,6	kW	
Rated Voltage	73		
Rated Current	15,1	A	6%
Rated Speed	2000	rpm	$<\!R_{SC}$.
Resistance at 20° C - Ph to Ph	0.59	Ω	
Inductance - Ph to Ph		mH	
Number of poles		poles	TRIPODATORIO DE CARDO
Number of turns per phase	72	turns	

Figure 2. PMSG characteristics and its stator windings.

Figure 3. Inter‐turn faulty PMSG model expressed in Park's frame.

3. SIMULATION PROCEDURE

3.1. Simulation Test Bench Setup

The PMSG studied in this paper is an existing threephase 3.6 kW four-pole Permanent Magnet Synchronous Machine (Figure 3). Stator windings of PMSG are modified with additional connection points in the stator coils on Phase A, allowing the introduction of several inter-turn short-circuit levels using a short-circuit resistance *RSC* (4%, 8%, 12% and 16% of short-circuit turns respectively corresponding to 3, 6, 9 and 12 turns on 72 whole turns of a stator winding).

The faulty PMSG model used for simulation is built using the Electrically Coupled Magnetic Circuit (ECMC) method [12]. It consists in a semi-analytical computation of the PMSG inductances from the stator winding layout including additional connections for inter-turn short-circuit generation.

The simulation test bench is set with SABER software in order to generate various operating points for the faulty PMSG model (Figure 4). It is composed of a balanced R-L load to vary the electrical power and the power factor, an unbalanced R load to create power unbalanced and a harmonic load including a three-phase diode bridge rectifier with resistive load to generate current harmonics.

3.2. Simulation Results

As shown in Figure 5, where a 16% inter-turn shortcircuit is generated at $t = 0.5$ s on phase A, each estimated parameter *ns/c* is impacted.

Indeed, a modification of their mean value and large oscillations at twice the electrical frequency appear as soon as the winding fault occurs (Figure 5b). It is also noticeable that *ns/cA* is not the only estimated parameter sensitive to the short-circuit: *ns/cB* and *ns/cC* are also modified in a lesser extent due to some simplifying assumption about the faulty PMSG model used for the parameters estimation. However, the fault can clearly be located on phase A in this test.

According to this test, the proposed fault indicator to

Figure 4. Simulation test bench scheme.

Figure 5. Response to a 16% inter-turn short-circuit (I_{LOAD} = 10 A per phase, 40 Hz). (a) Short‐circuit current. (b) Estimated parameters. (c) Fault indicator.

detect inter-turn short-circuits expressed in (8) is based on the sum of these three estimated short-circuited turn ratios (Figure 5c). A moving average calculation over one electrical half period is added to filter the fault indicator and improve its robustness.

$$
Fault_indicator = \sum_{i=A,B,C} \langle /n_{s/c} / i \rangle_{T/2} \tag{8}
$$

Figure 6 shows the evolution of this fault indicator in the healthy case (for $t < 0.5$ s) and in the faulty case (for t > 0.5 s) with several values of short-circuited turns on phase A. In the healthy case, the fault indicator remains close to 0, reflecting a safe PMSG. In faulty cases, the fault indicator increases with the number of short-circuited turns, which confirms that the fault detection is trickiest for a few number of short-circuited turns. However, shortcircuit detection remains possible even for 4% of shortcircuited turns in comparison with the fault indicator behavior in the healthy case. Moreover, time response of the proposed fault indicator remains quick (about 20 ms) in spite of the filtering characteristic of the moving average calculation.

In the following section, the effectiveness of this fault indicator is evaluated against several operating points and several inter-turn short-circuit.

4. FAULT INDICATOR PERFORMANCE ASSESSMENT

4.1. Robustness Assessment

In order to evaluate the robustness of each estimated parameter to the inter-turn short-circuit diagnosis, several tests based on Figure 4 have been made and are listed below:

Test 1 - Frequency variation from 30 Hz to 60 Hz with 10 Hz step and 10 A load current per phase.

Test 2 - Power variation from 15 A to 0 A load current per phase with 2.5 A step and 40 Hz electrical frequency.

Test 3 - Power factor variation from 1 to 0.8 with 0.05 step at 40 Hz and 10 A load current per phase.

Test 4 - Unbalanced load variation from -5 A to +5 A unbalanced load current with 2.5 A step at 40 Hz and 10 A load current in other phases.

Test 5 - Harmonic load variation from 0% to 20% of nominal current harmonic distortion variation with 5% step at 40 Hz and 10 A load current phases per phase.

These scenarios are related to specifications that could be encountered in embedded electrical networks. The robustness is then characterized by the ability of the EKF algorithm to properly separate the healthy and the faulty cases whatever the operating conditions. To quantify this ability, healthy and faulty simulations are performed under the same operating conditions and the worst indicator's value is kept for each test (i.e. the highest indicator's value in the healthy case and the lowest indicator's value in the faulty case). For example, Figure 7 shows the evolution of the fault indicator for various operating electrical frequencies. In this test, the highest fault indicator values in healthy cases appear at the lowest frequency whereas the lowest fault indicator values in faulty cases appear at the

Figure 6. Fault indicator response to various inter-turn short-circuits (I_{LOAD} = 10 A per phase, 40 Hz). Figure 7. Robustness spider chart for the proposed fault indicator.

Figure 8. Fault indicator response to various electrical frequencies ($I_{LOAD} = 10$ A per phase). (a) Electrical frequency. (b) Short‐circuit current. (c) Fault indicator.

highest frequency.

The following spider chart (Figure 8) summarizes the robustness of the proposed fault indicator against electrical network variation, using the worst indicator's values for each test, to compare healthy and faulty operation with several short-circuited turns. It indicates that an inter-turn short-circuit fault can be easily distinguished from the healthy PMSG whatever the operating conditions and even for a few number of short-circuited turns.

According to these robustness tests, a detection threshold is set at twice the maximum of the fault indicator in healthy condition for each robustness test, in order to avoid any false alarms in healthy operation. This maximum fault indicator value in healthy case appears at the lowest frequency in the frequency tests. Thus, the detection threshold is set to 2.5% and is also represented in Figure 8.

4.2. Sensitivity Assessment

The sensitivity assessment consists in the evaluation of the short-circuit current needed to detect the inter-turn fault. It can be calculated according to the detection threshold previously determined. Figure 9 shows this sensitivity test in which the short-circuit resistance *Rsc* is used to vary the inter-turn short-circuit severity.

This test confirms that the fault detection is trickier and more serious for a few number of short-circuited turns. Indeed, in relation to the 15 A rated current of the studied PMSG, a fault current of 45 A is required to detect a 4% inter-turn short-circuit compared to a 15 A fault current required to detect a 16% inter-turn short-circuit. However, stator windings of a PMSG can easily withstand three times the rated current during the detection time (about 20ms). Moreover, these results show that, even for a few number of short-circuited turns, an early detection of a resistive interturn short-circuit is possible with the proposed fault indicator.

5. CONCLUSIONS

This paper describes a method for inter-turn short-

Figure 9. Sensitiveness assessment for the proposed fault indicator (I_{LOAD} = 10 A per phase, 40 Hz). (a) Fault indicator. (b) Short‐circuit current.

circuit detection in stator windings based on a short-circuit turns estimation on a faulty PMSG model using an EKF algorithm. This fault diagnosis allows a fast and robust detection of inter-turn short-circuit, whatever the operating conditions on a non ideal electrical network even for a few number of short-circuited turns. Moreover, this fault indicator allows to localize the faulty phase and is also sensitive to resistive inter-turn short-circuit which could prevent a dead short-circuit.

Moreover, experimental tests have also been done with the short-circuit current limited to 25 ARMS for safety considerations. Experimental results, available in [13], are quite similar to simulation ones and confirm the good representativeness of the simulation model even for a resistive inter-turn short-circuit.

Finally, the prospect of this study is the application of this fault indicator on a 45 kW, 400 Hz – 800 Hz PMSG.

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